

DIRECT SYNTHESIS CLOCK GENERATION
CIRCUITS AND METHODS

ABSTRACT:

[0040] A clock generator includes input circuitry for receiving an input signal and generating a memory address therefrom. A memory stores digital data indexed by the memory address which represents at least a portion of an analog clock. A digital to analog converter converts data retrieved from the memory to generate the analog clock which is then filtered by a filter and then converted into digital output clock.

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